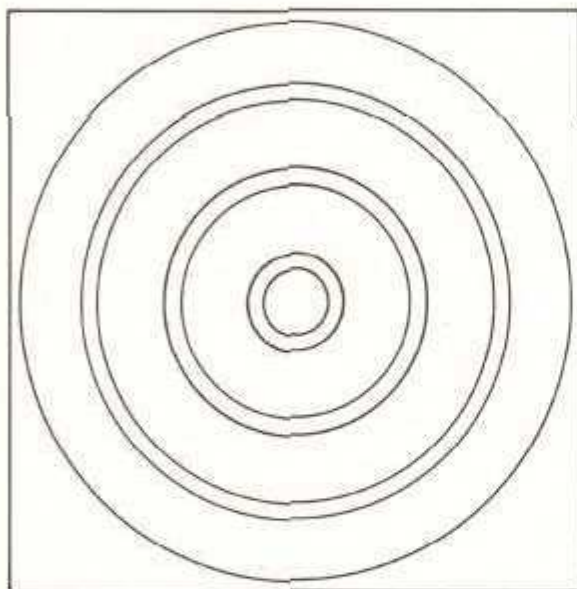


PACT SYSTEM

4024 CPU

ELBIT



The Elbit PACT Computer is a powerful 16-bit word minicomputer with a full repertoire of arithmetic, logic and data transfer commands. It is a computer designed for growing organizations. Compact though it is, the speed and capability of this minicomputer exceeds that of many medium-sized computers.

STANDARD FEATURES

- LSI Technology
- 140-nsec Micromemory Cycle
- 700-nsec Average Memory Cycle
- Semiconductor RAM
- Memory Parity Bit
- 16-Bit Data Word, Write-Protect Bit Word and Block of 2K Words via Memory Mapping.
- Memory Size to 128K Words (256K Bytes) with 64K-Word Direct Addressability
- Memory Modules: 32K Words (64K Bytes) and 16K-Word (32K Bytes)



- Eleven Addressing Modes
- Mapping Memory Management with Paging Registers
- Several General Purpose Registers (Accumulators)
- Hardware Integer Multiply/Divide
- Word-, Byte- and Bit-Instruction
- 16-Level Vectored Priority Interrupt System
- Three Data- Transfer Modes
 - Programmable
 - Automatic
 - Direct Memory Access
- Programmable Realtime Clock
- Autoload from Selectable Devices
- Operator Console
- CPU, Memory, and up to 11 I/O Controllers each of several Peripherals, all housed in one 19" Cabinet along with the Power Supply and Cooling Fans.

OPTIONS

- Operator Console and Interface
- 23-Bit Memory Word for Single Error Correction/Multiple Error Detection (ECC)
- Memory Options:
 - Memory without ECC
 - Read: 590 ns Write: 990 ns
 - ECC Memory
 - Read: 630 ns Write: 1035 ns
- Memory Expansion up to 256K Words (512K Bytes)

Specifications Subject To Change Without Notice.

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